

FIG. 1

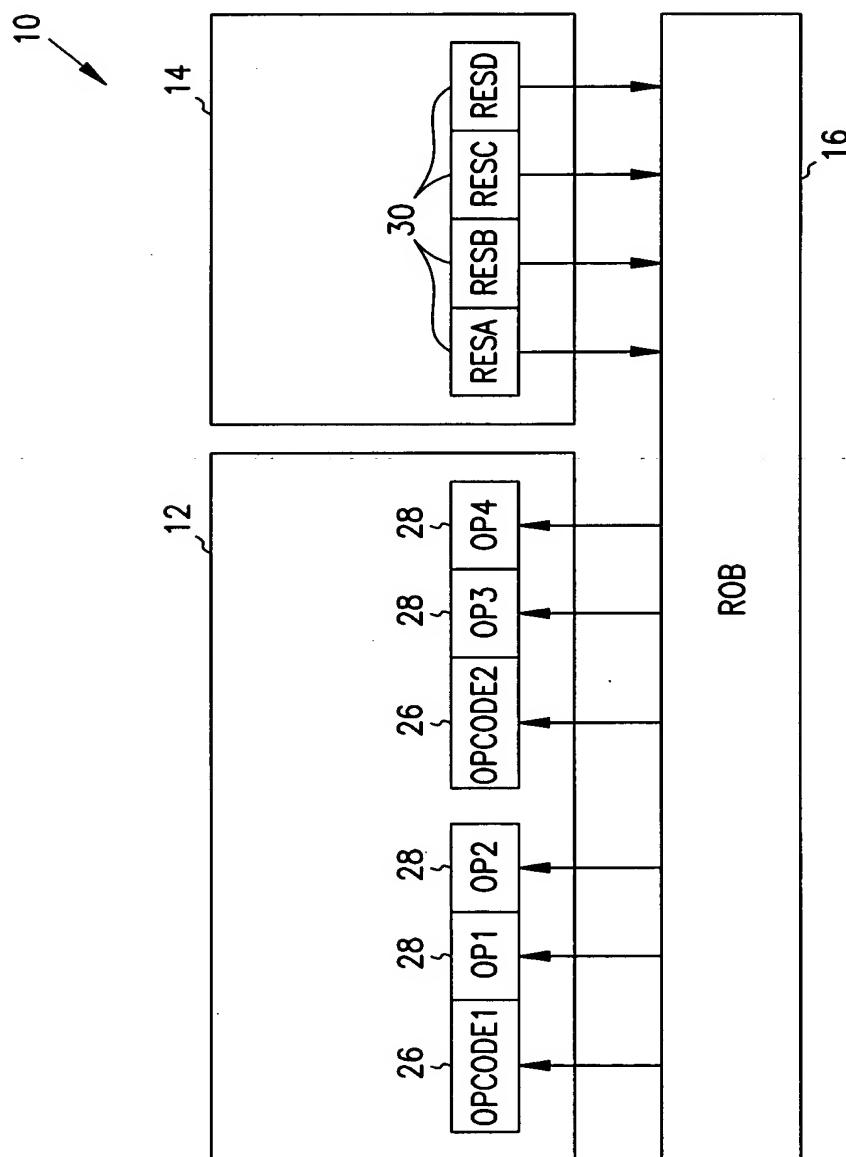


FIG. 2

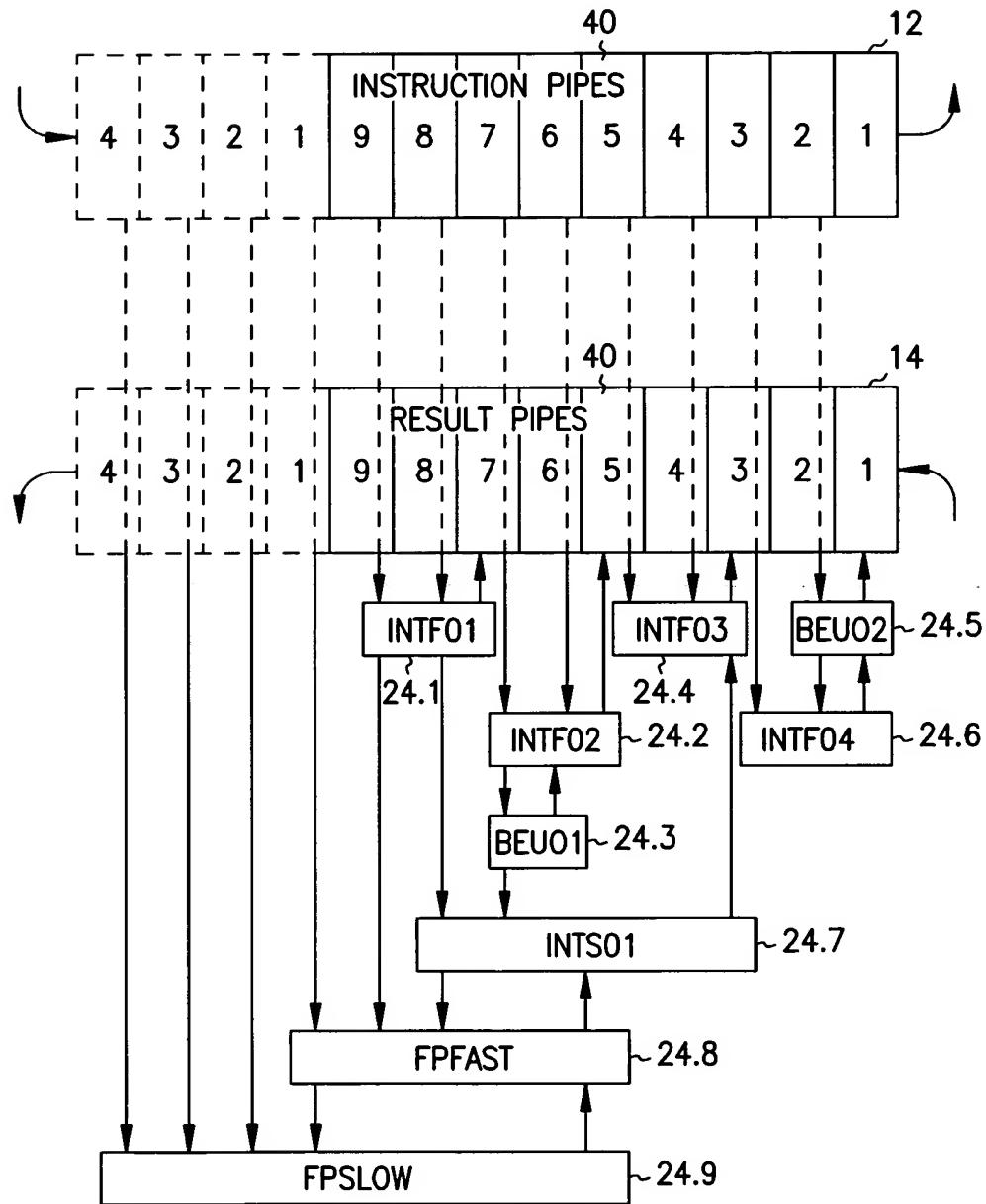


FIG. 3

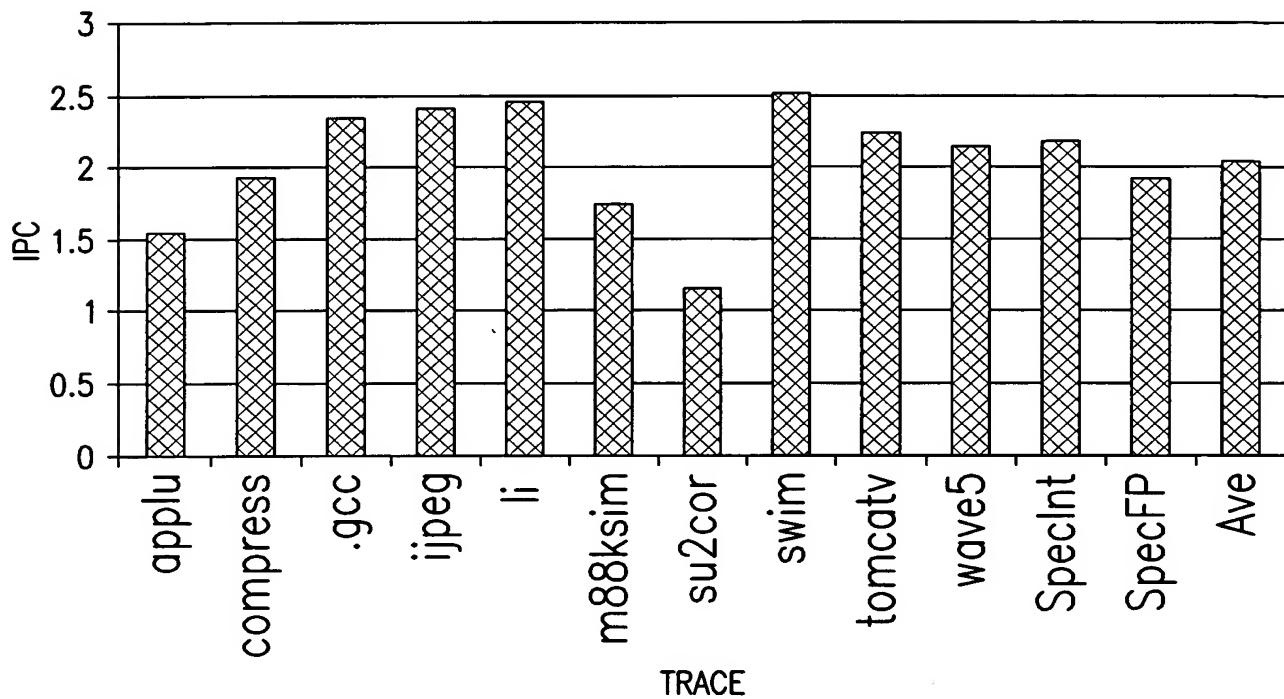


FIG. 4



FIG. 5

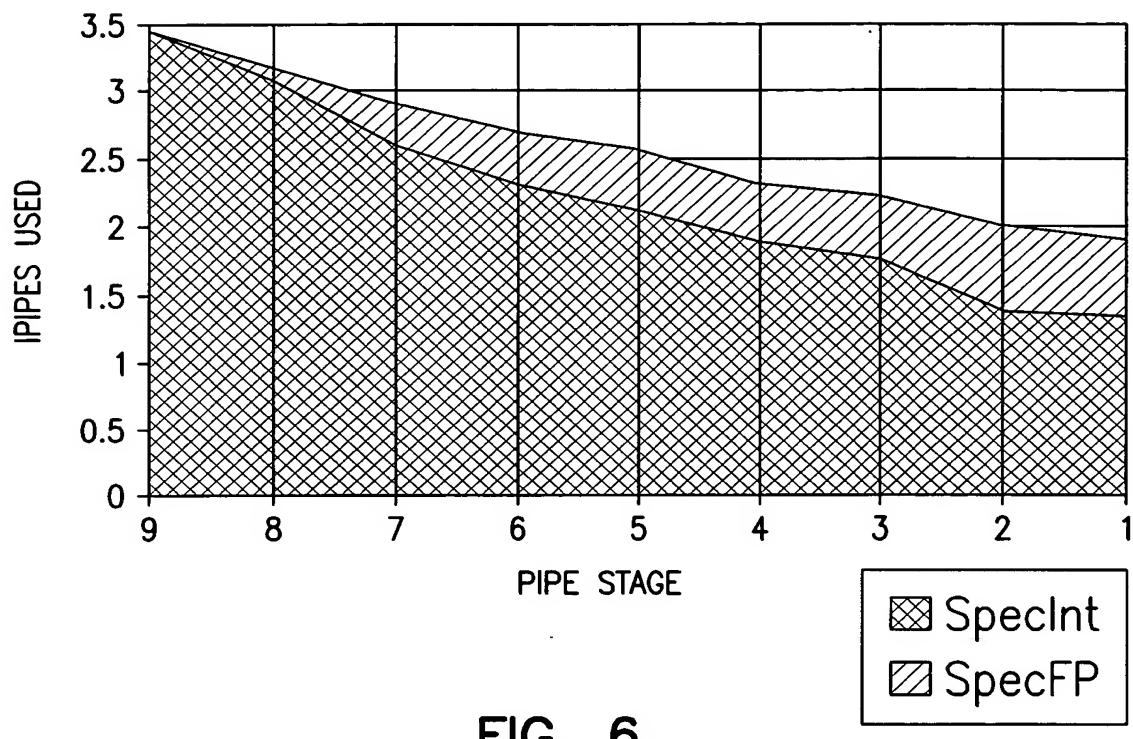


FIG. 6

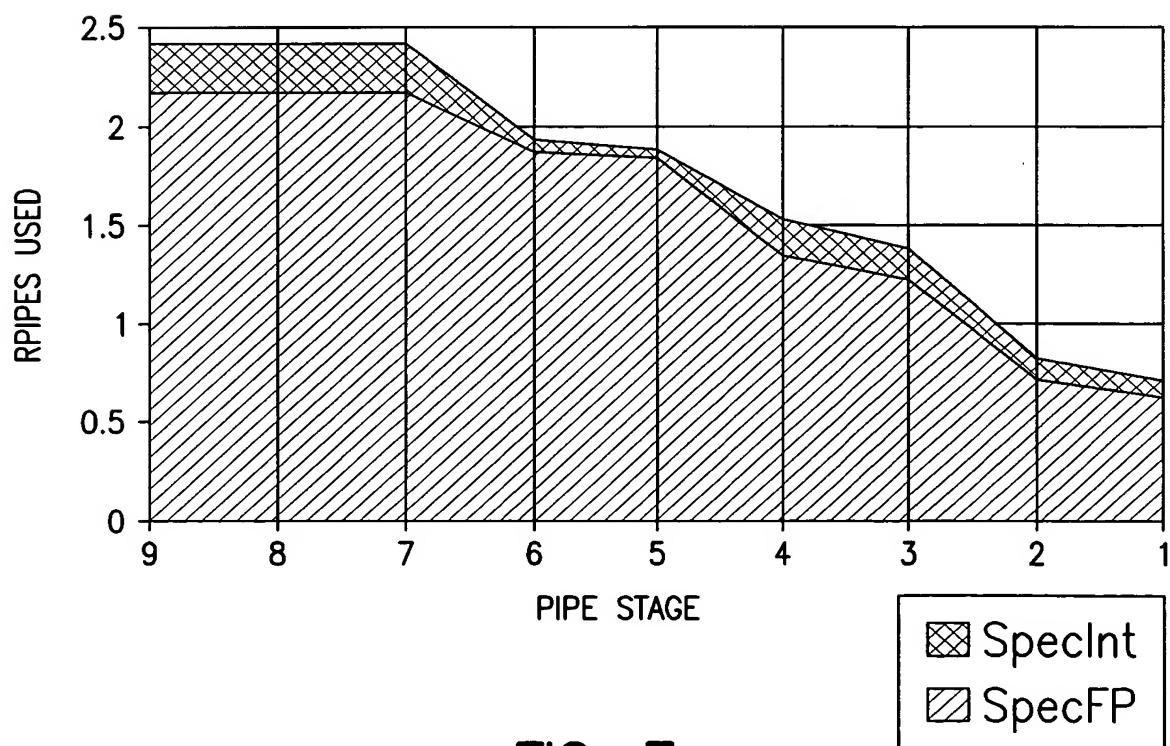


FIG. 7

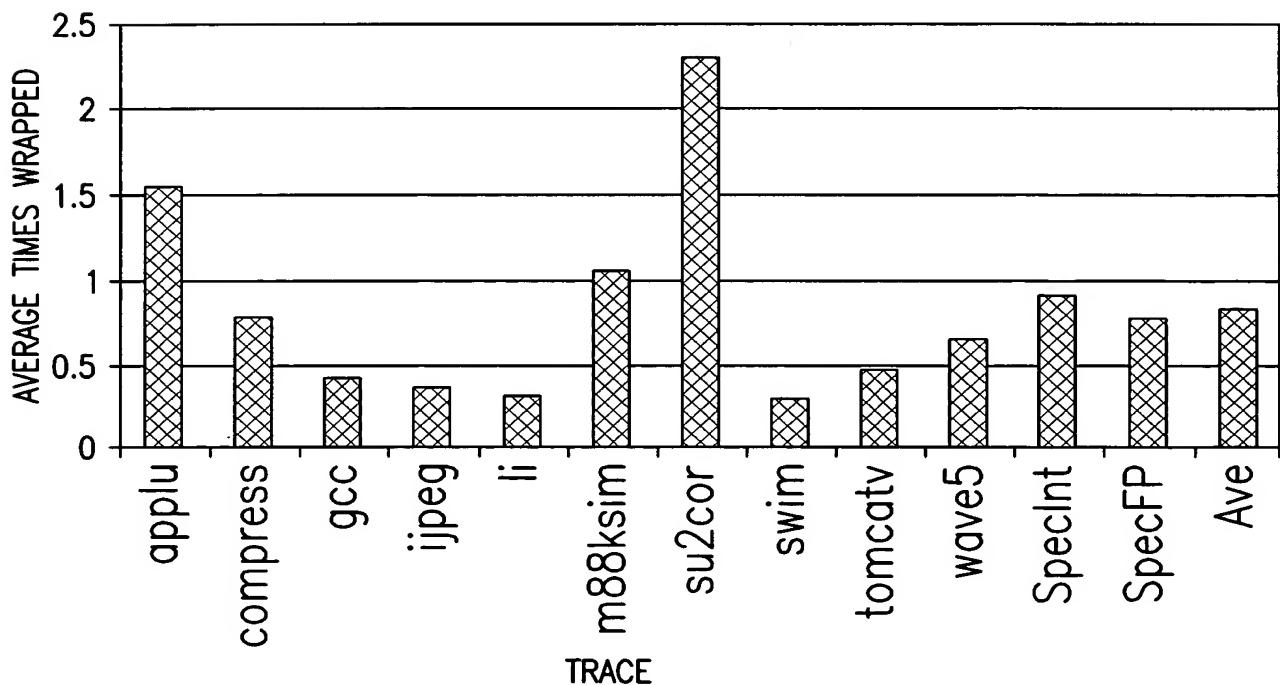
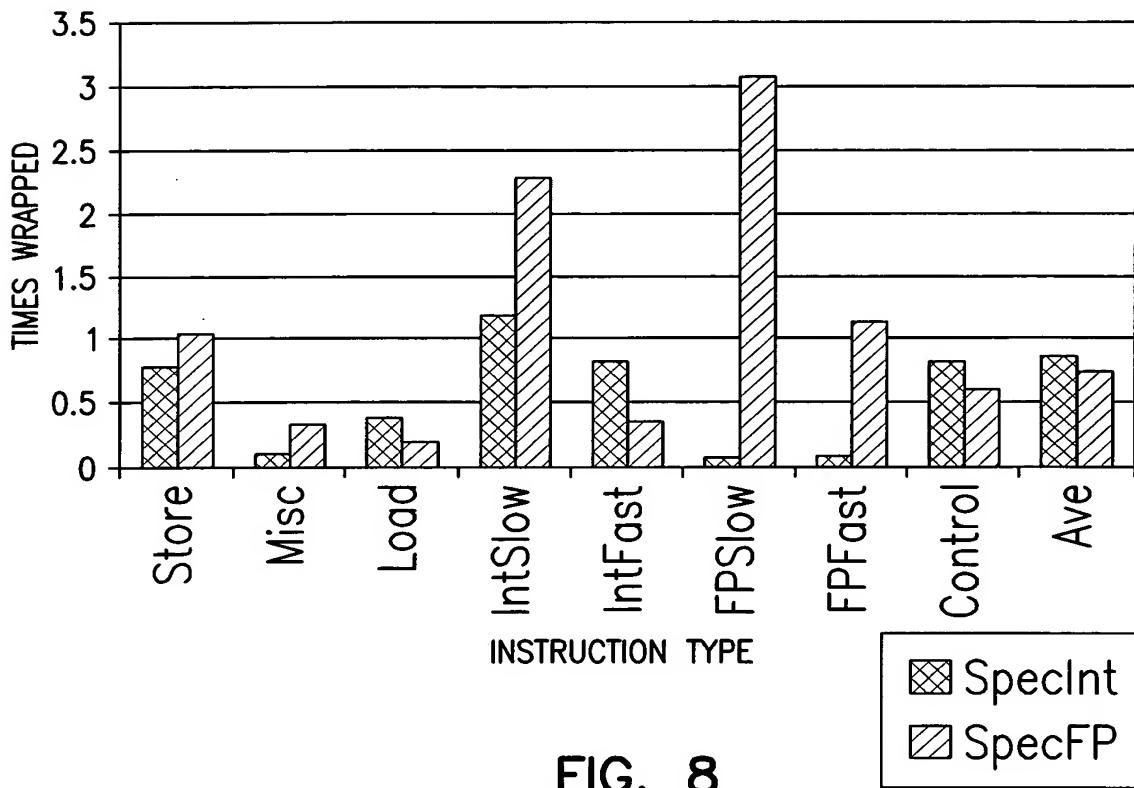


FIG. 9

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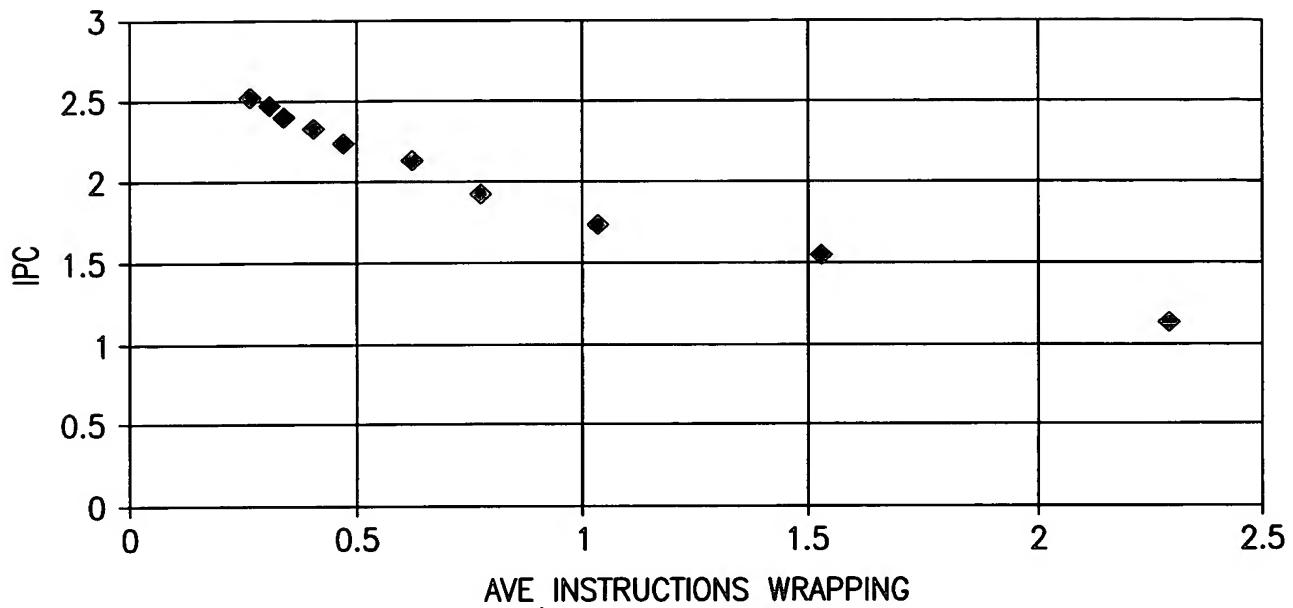


FIG. 10

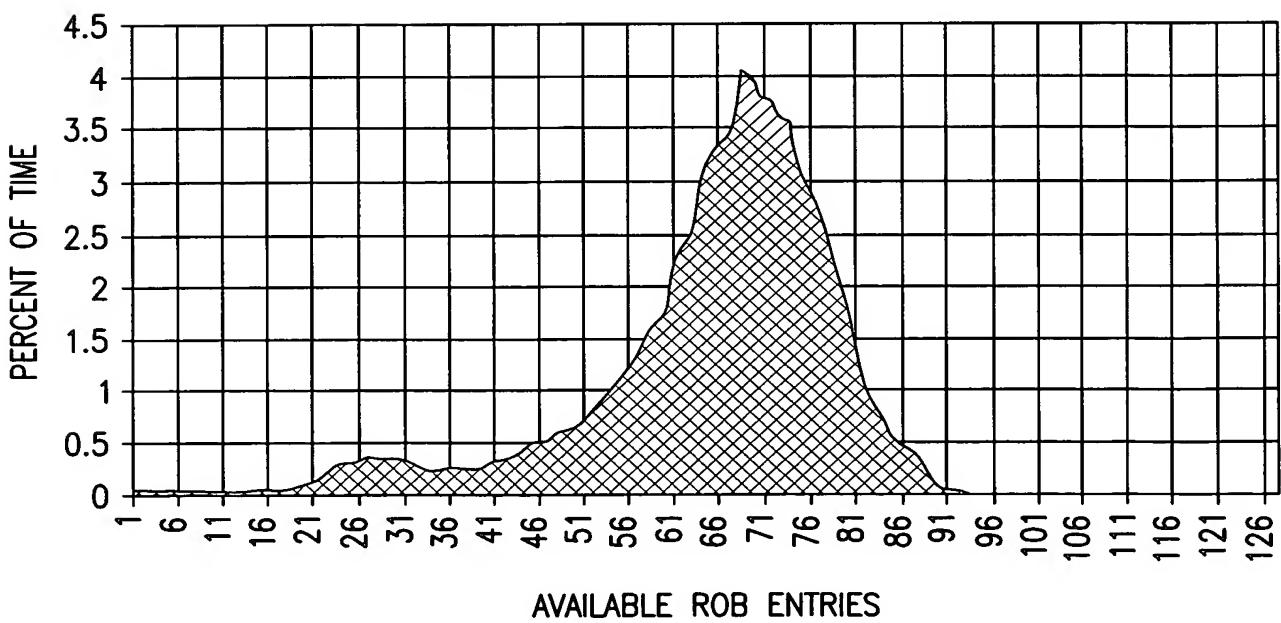


FIG. 11

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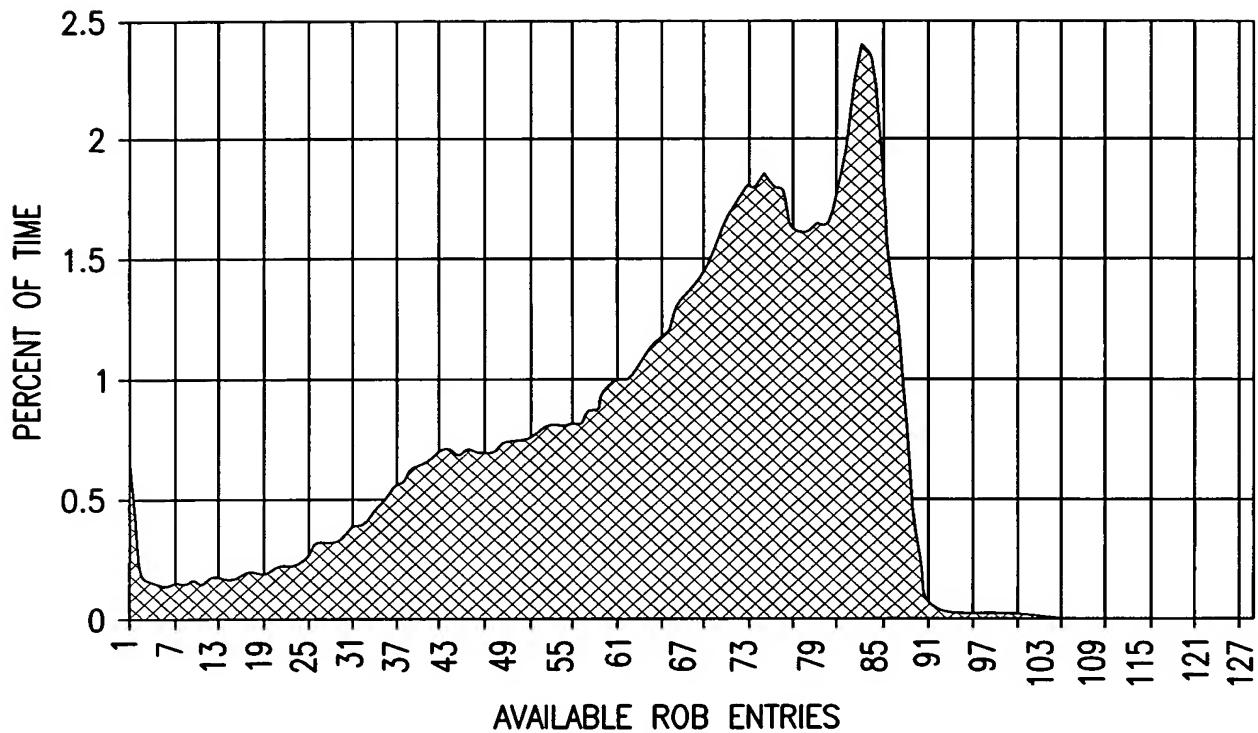


FIG. 12

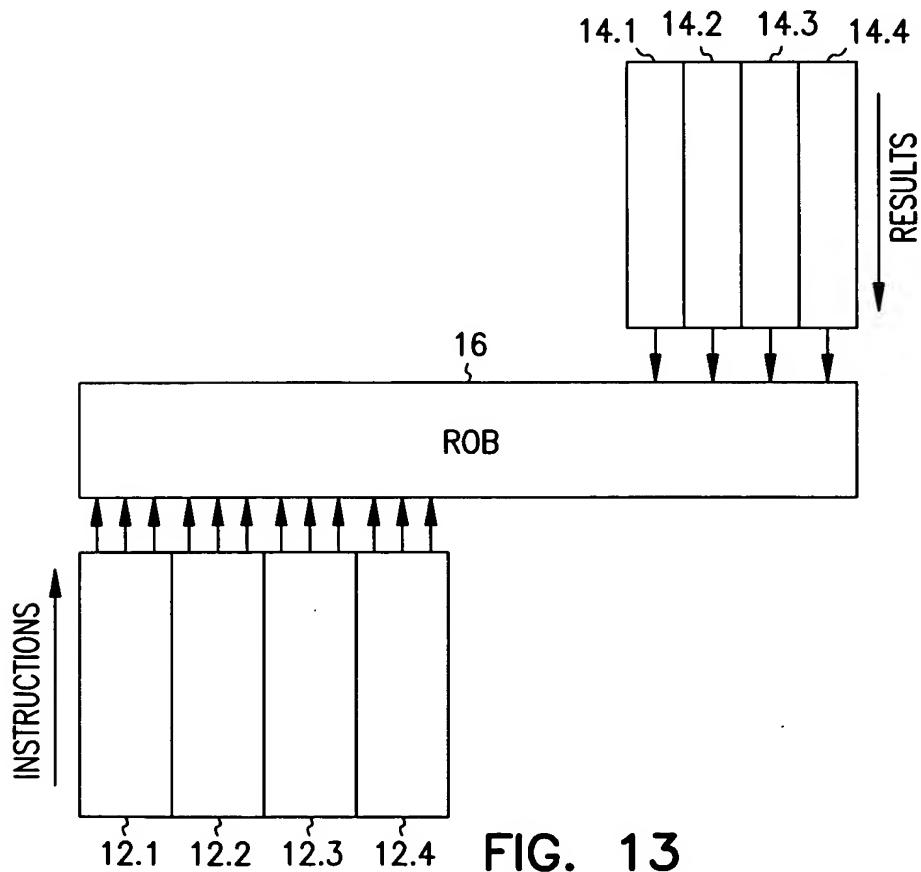


FIG. 13

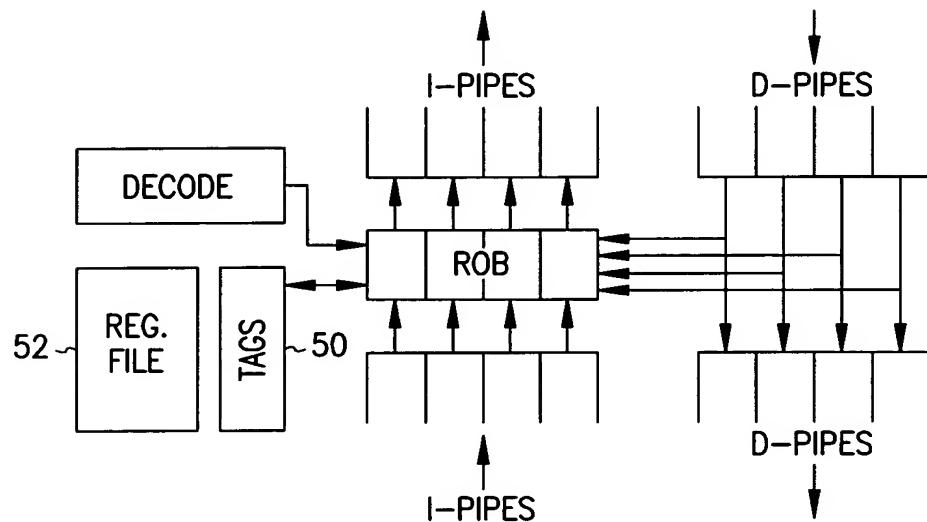


FIG. 14a

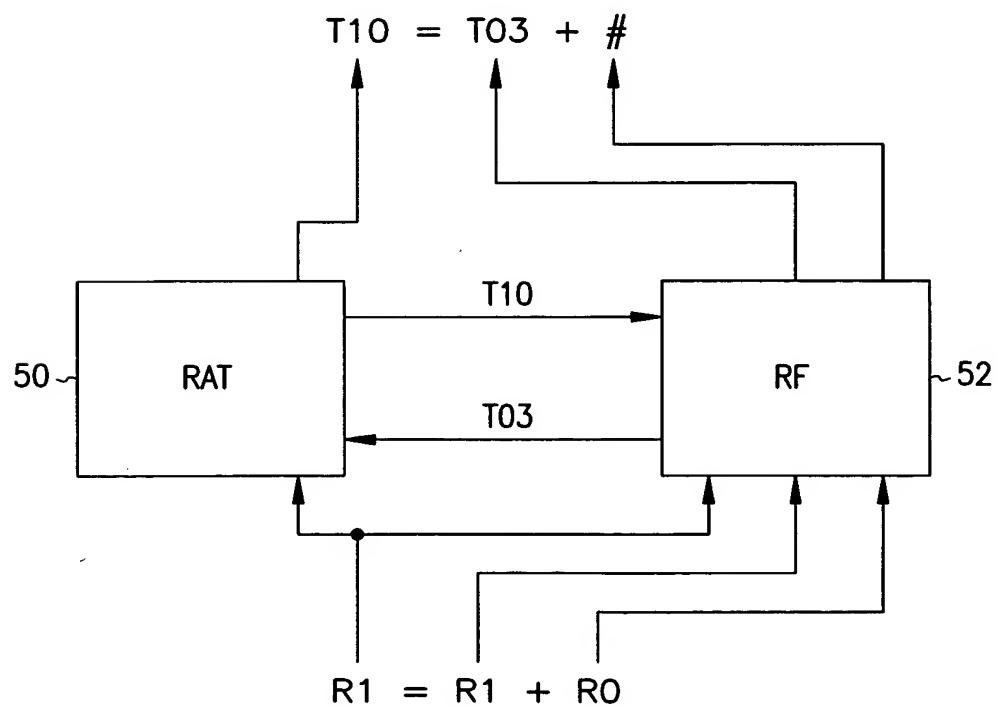


FIG. 14b

PIPE REGISTER LAST			VALID	ALIAS
HEAD → 0	0	XX	XX	~50
	1	1	XX	XX
	2	0	XX	XX
TAIL → 3	0	R1	03	~52
	(RAT)			(RF)

FIG. 15a

PIPE REGISTER LAST			VALID	ALIAS
HEAD → 0	1	R1	03	~50
	1	1	XX	XX
	2	0	XX	XX
TAIL → 3	0	R1	03	~52
	(RAT)			(RF)

FIG. 15b

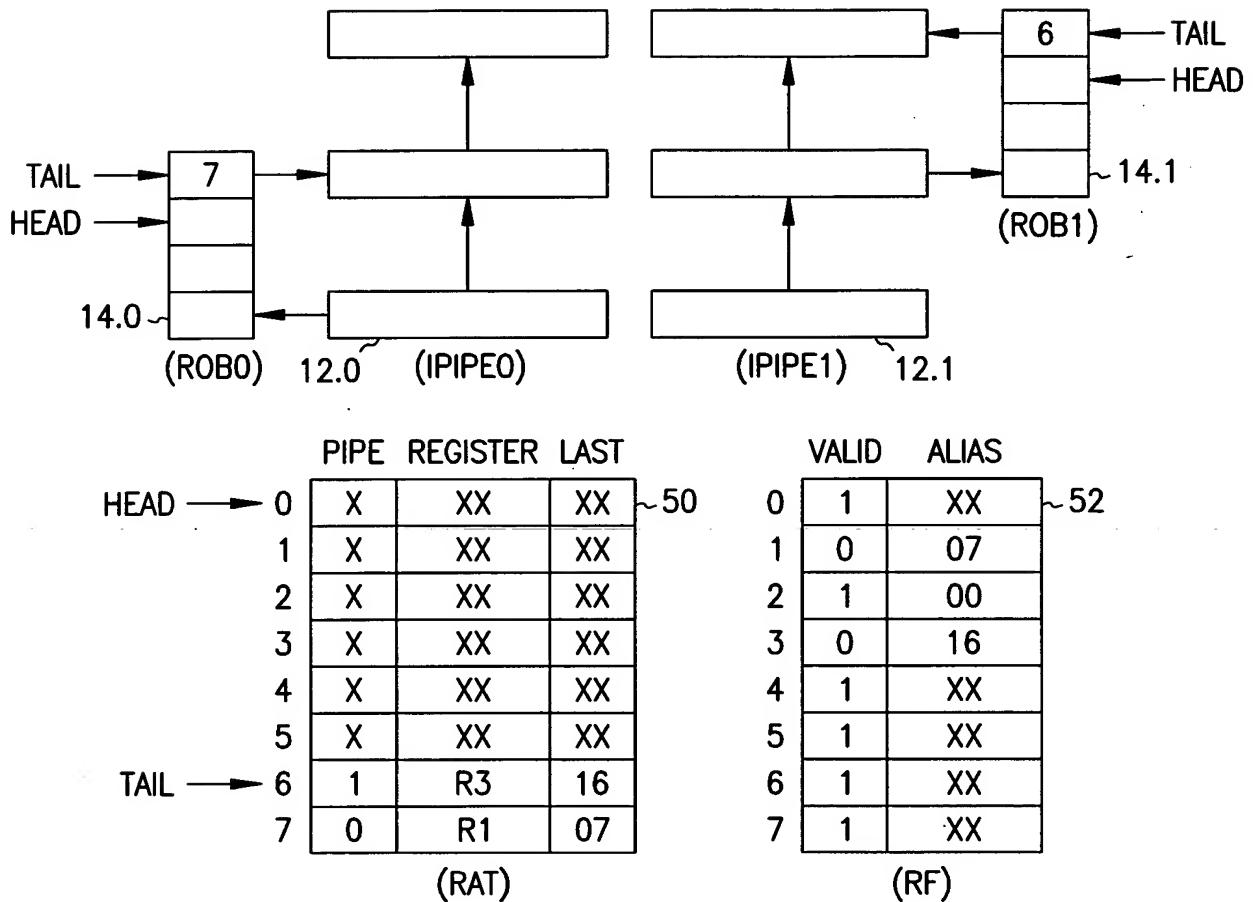


FIG. 16a

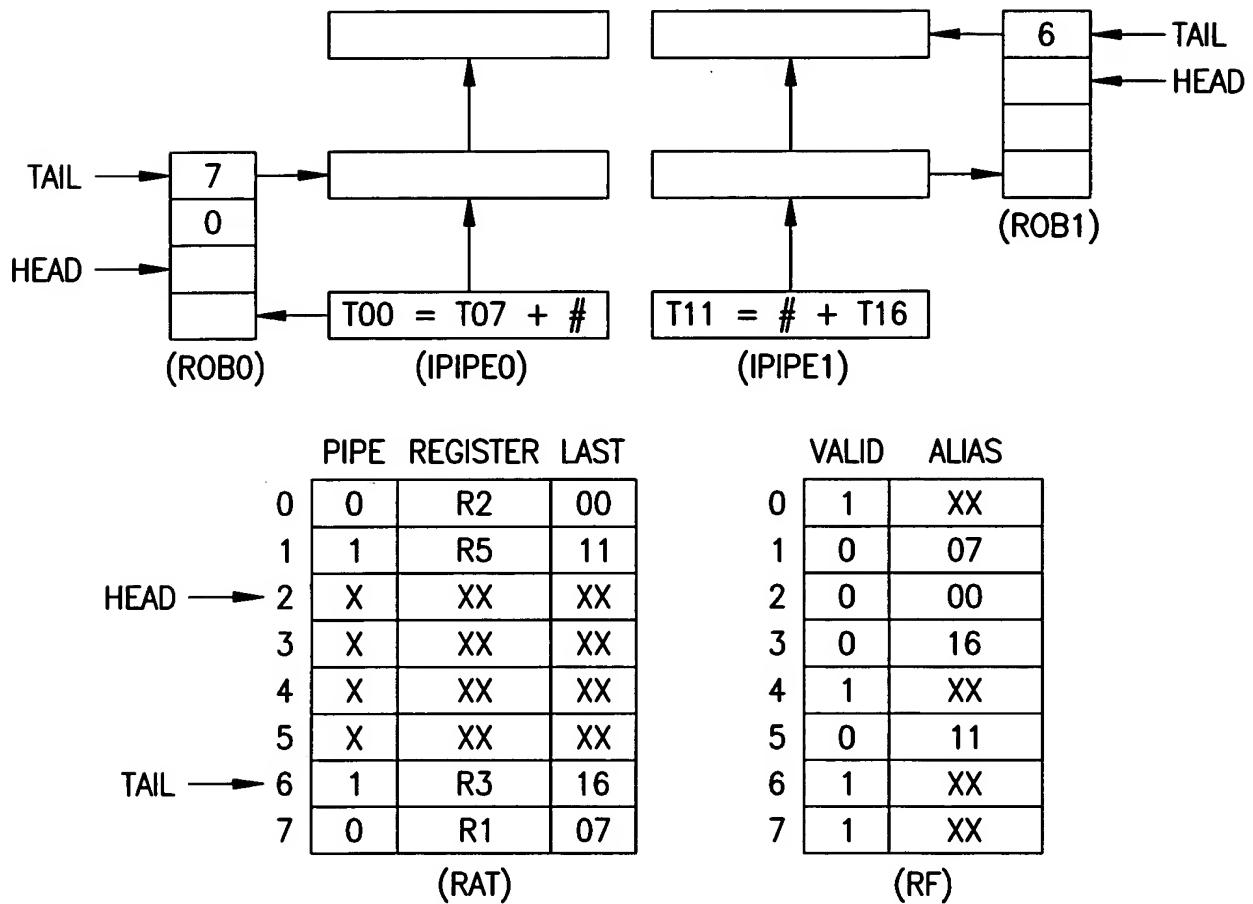


FIG. 16b

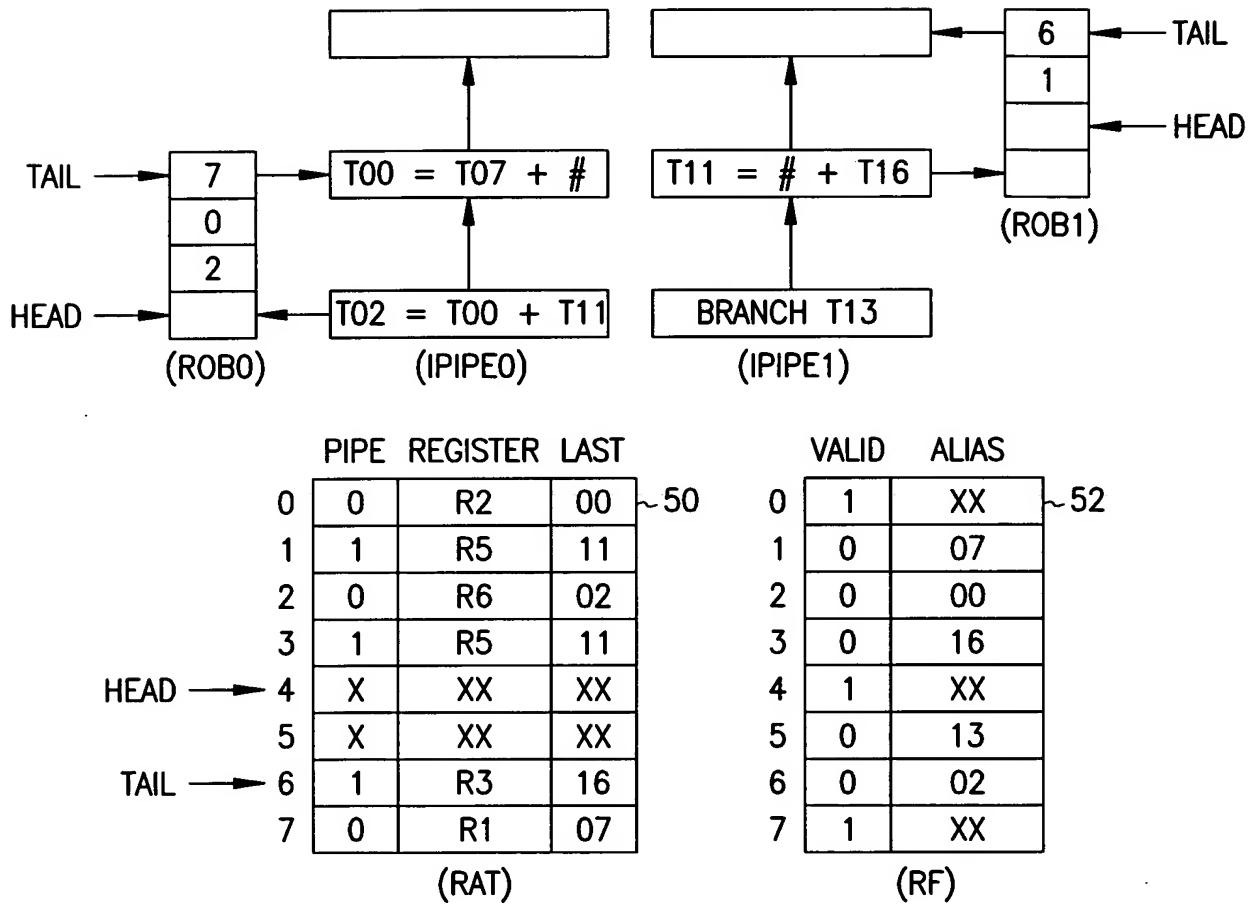


FIG. 16c

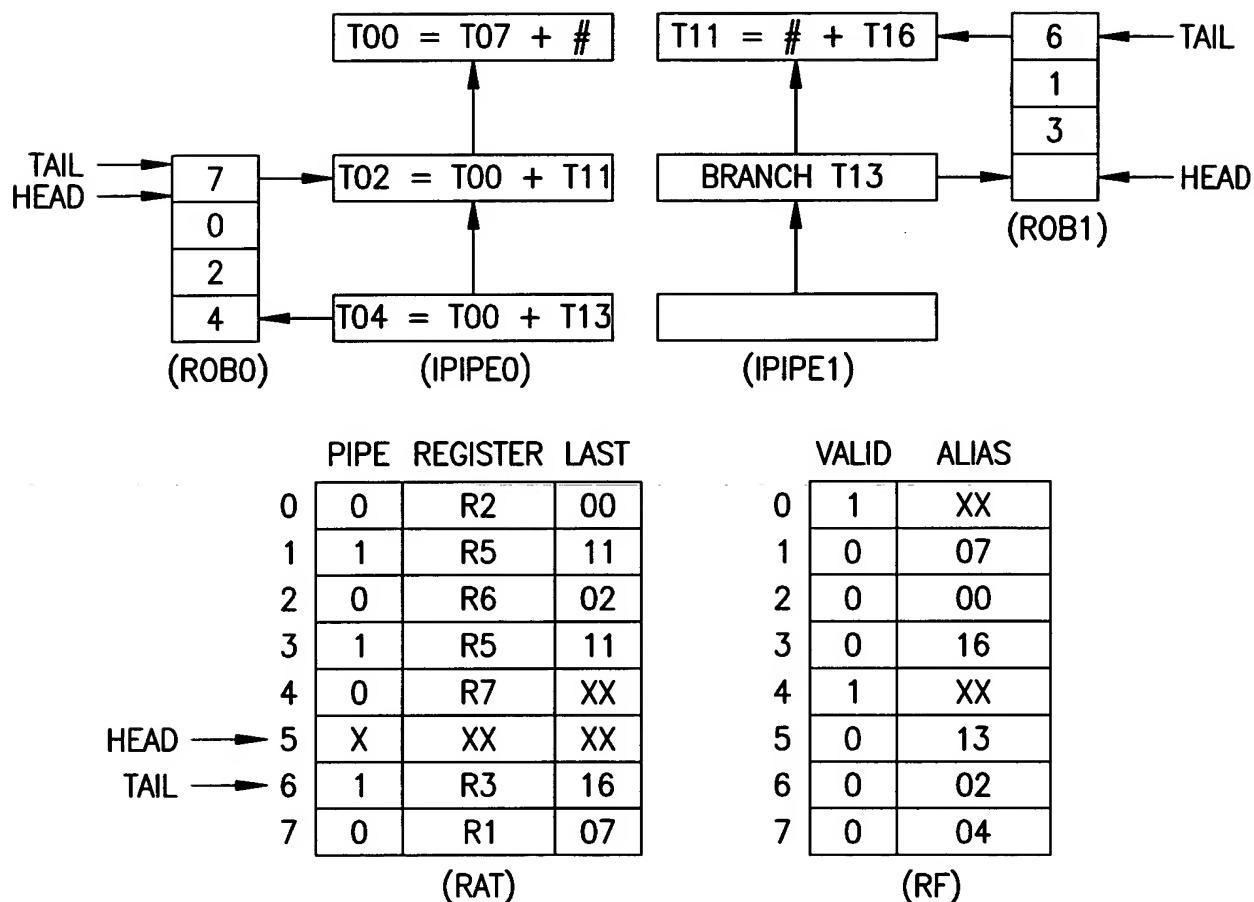


FIG. 16d

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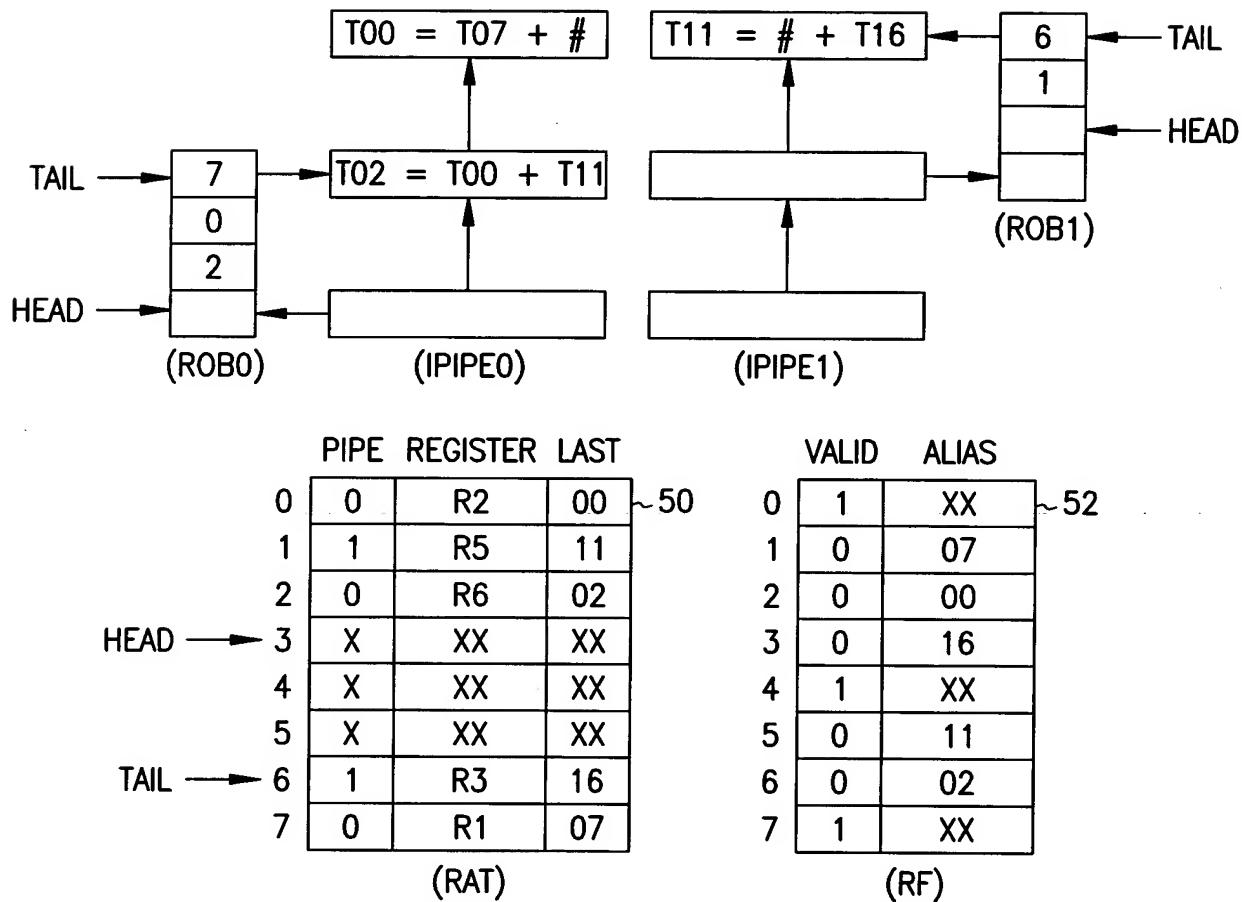


FIG. 16e

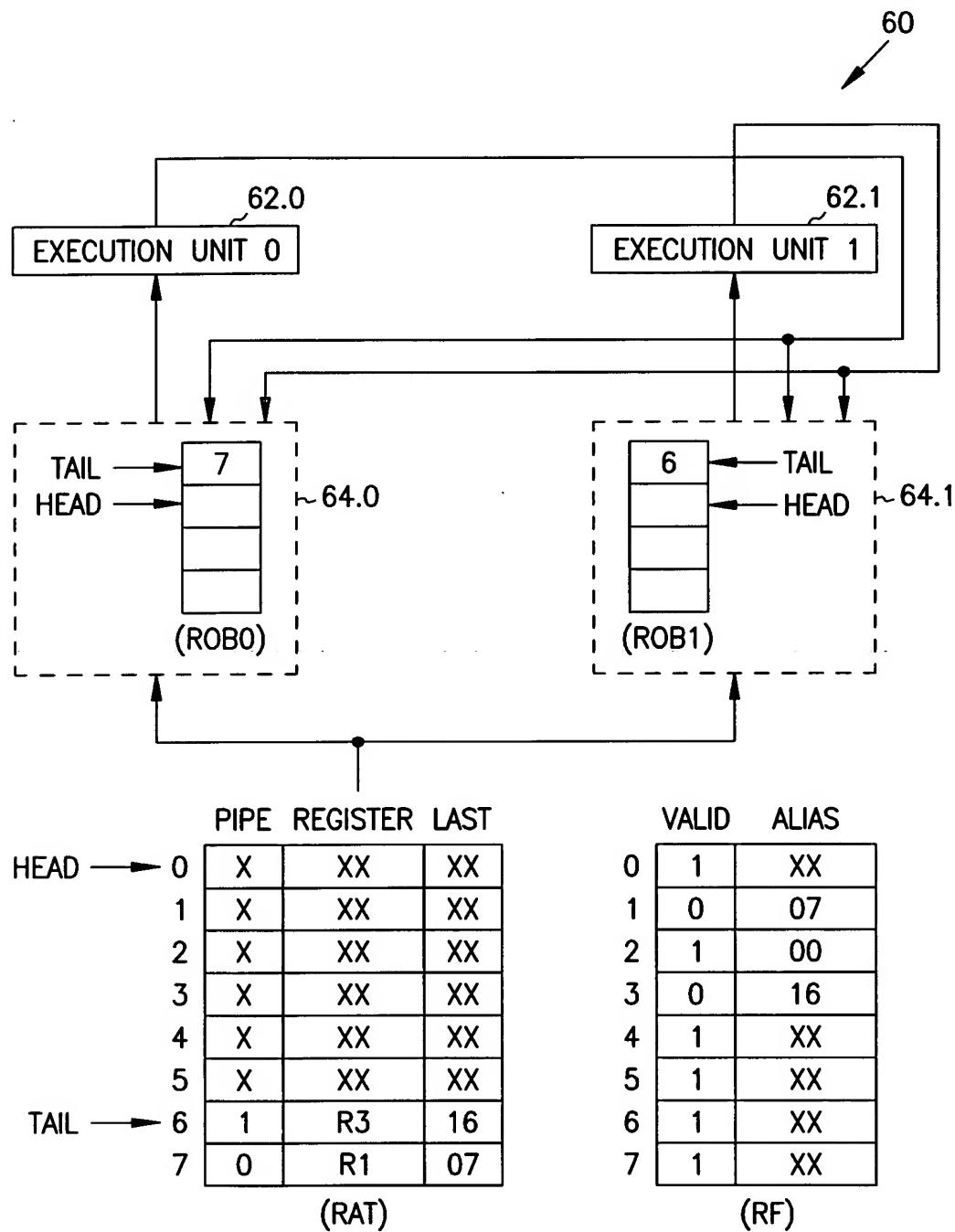


FIG. 17

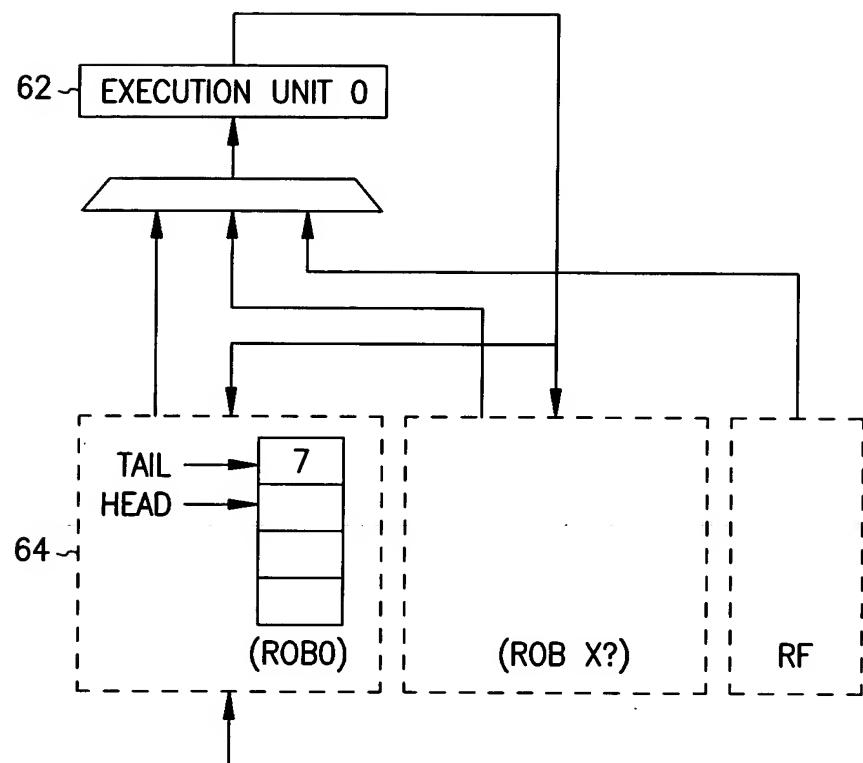


FIG. 18

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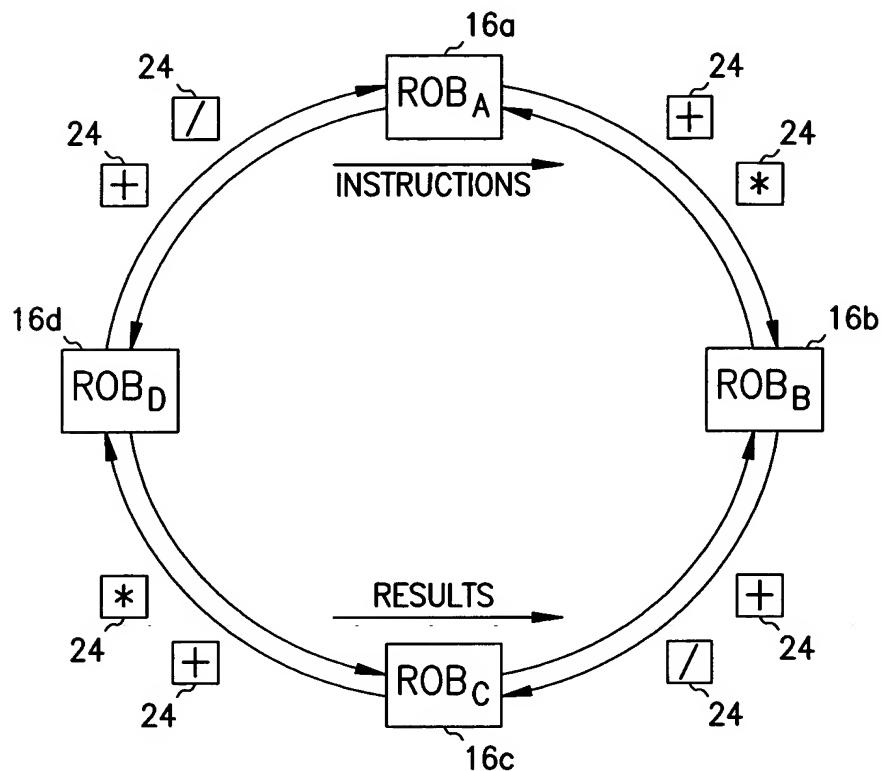


FIG. 19

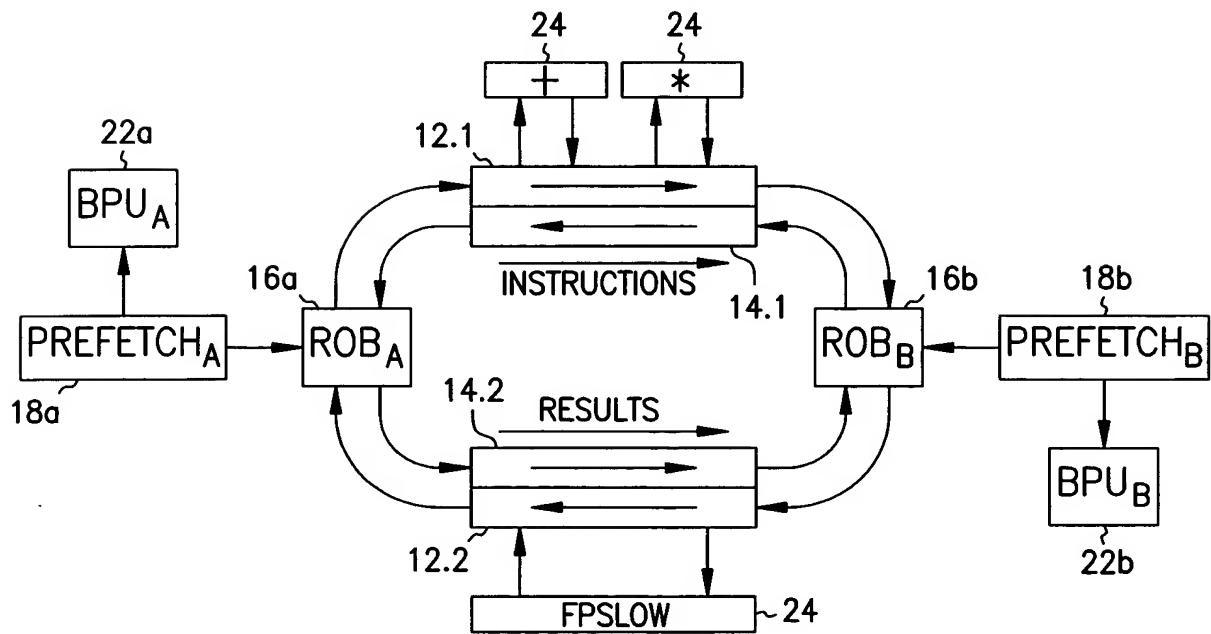


FIG. 20

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	T0	T1	T2	T3	T4	T5	T6	T7	T8	T9
LOAD R1, X	V/P (CORRECT)						eV/NP	RET		
LOAD R9, Y	V/P (INCORRECT)						eV/NP	RET		
ADD R2, R1, R2		eV/P						V/NP	RET	
ADD R3, R4, R2			eV/P					V/NP	RET	
ADD R8, R9, R4				eV/P				eV/NP	RET	
ADD R7, R8, R4					eV/P			eV/NP	RET	
ADD R5, R5, R5						eV/NP			RET	
...										

FIG. 21